

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**  
**In the DO/EO/US**

In re the Application of:

Oussama LAOUAMRI et al.

Art Unit: not yet assigned

Application No.: 10/589,489

Examiner: not yet assigned

Filed: 06/25/2007

Attorney Dkt. No.: 71247-0063

For: INTEGRATED CIRCUIT CHIP WITH COMMUNICATION MEANS ENABLING  
REMOTE CONTROL OF TESTING MEANS OF IP CORES OF THE  
INTEGRATED CIRCUIT

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

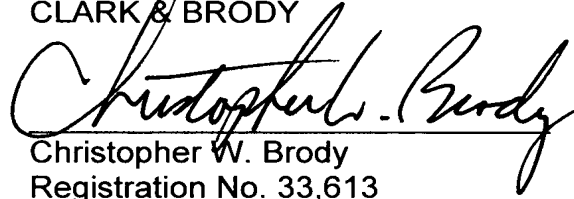
Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

As permitted by Rule 1.98(a), copies of the U.S. references are not included herewith.

Applicants respectfully submit that this disclosure is being filed in accordance with 37 C.F.R. 1.97(b), therefore, no fee is required.

Respectfully submitted,  
CLARK & BRODY

  
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Date: September 11, 2007

FORM PTO-1449  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  LIST OF REFERENCES CITED BY APPLICANT  <i>(Use several sheets if necessary)</i>	ATTY. DOCKET NO.	SERIAL NO.
	<b>71247-0063</b>	<b>10/589,489</b>
	APPLICANT <b>Oussama Laouamri</b>	
	FILING DATE <b>06/25/2007</b>	GROUP ART UNIT <b>Not yet assigned</b>

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	<b>1.</b>	<b>2003/0120986</b>	<b>06/26/2003</b>	<b>Whetsel</b>			

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	<u>TRANSLATION</u>		
							YES	NO	PART.

OTHER REFERENCES *(Including Author, Title, Date, Pertinent Pages, Etc.)*

	2.	O. Laouamri et al., "Enhancing Testability of System on Chips Using Network Management Protocols", Proceedings of the Design, Automation and Test in Europe Conference and Exhibition (2004)
	3.	O. Laouamri et al., "Hardware-based Network Management Framework for Monitoring and Testing of System-On-Chips", 8th International Conference on Telecommunications, June 15-17, 2005
	4.	O. Laouamri et al., "Remote Testing and Diagnosis of System-on-Chips Using Network Management Frameworks", © 2007, EDAA
	5.	O. Laouamri et al., "Towards a Complete SNMP-Based Supervision of System-on-Chips", Journal of Network and Systems Management, Vol. 13, No. 4, Dec. 2005, pgs. 373-386
EXAMINER		DATE CONSIDERED
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		